

30932 U.S. PTO
10/021259



12/19/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM FILING DATE CLASS SUBCLASS GAU EXAMINER
10021259 12/19/2001 438 5 282.9 Kilday

****APPLICANTS:** Hasegawa Hiroyuki; Yamaoka Tomonori; Ishihara Yoshio; Masusaki Hiroshi; Satou Takayuki; Suzuki Katsumasa; Tokunaga Hiroki;

****CONTINUING DATA VERIFIED:**
THIS APPLICATION IS A DIV OF 09/651,255 08/30/2000

** FOREIGN APPLICATIONS VERIFIED:

JAPAN 11-246240 08/31/1999
JAPAN P2000-052555 02/28/2000
JAPAN P2000-052516 02/28/2000

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

P 284101 OSP-11726

TITLE : Method of purging CVD apparatus and method for judging maintenance of times of semiconductor production apparatuses

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	
		Print Claim for O.G.	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)

BEST AVAILABLE COPY